

Abstract

A display apparatus with an active matrix display panel which can suppress gate stress to prevent a degradation of the display quality. Specified at least one pixel section having a light emitting element to driven for light emission in one row is supplied with a data pulse indicative of a first gate voltage of a thin film transistor upon supply of a display scanning pulse. Subsequently, each pixel in the one row is supplied with a reset scanning pulse, and when supplying a reset scanning pulse, the at least one pixel section is supplied with a reset pulse indicative of a second gate voltage of the thin film transistor for making the polarity of a gate-to-source voltage or gate-to-drain voltage of the thin film transistor reverse to that during light emission driving.